





NPN SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

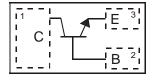
- Complementary PNP Type Available (BC857BLP)
- Ultra-Small Leadless Surface Mount Package
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections Indicator: Collector Dot
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Ordering Information: See Page 3 Marking Information: See Page 3
- Weight: 0.0009 grams







TOP VIEW (Internal Schematic)

DFN1006-3

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	50	V
Collector-Emitter Voltage	V_{CEO}	45	V
Emitter-Base Voltage	V_{EBO}	6.0	V
Collector Current	lc	100	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @T _A = 25°C	P_{D}	250	mW
Thermal Resistance, Junction to Ambient (Note 3) @T _A = 25°C	$R_{ hetaJA}$	500	°C/W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +150	°C

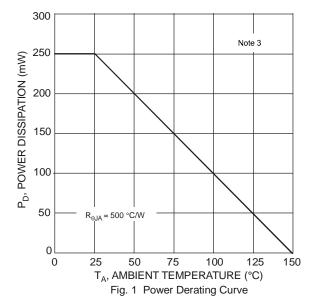
Electrical Characteristics @T_A = 25°C unless otherwise specified

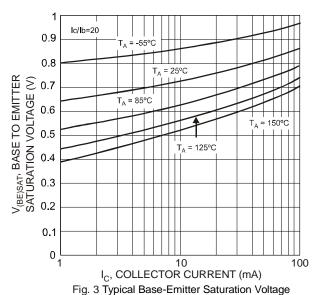
Characteristic (Note 4)	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	V _{(BR)CBO}	50	_	_	V	$I_C = 10\mu A, I_B = 0$
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	45	_	_	V	$I_C = 10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	6		_	V	$I_E = 1\mu A, I_C = 0$
DC Current Gain	h _{FE}	200	350	450	_	$V_{CE} = 5.0V, I_{C} = 2.0mA$
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	1	80 200	250 600	mV	$I_C = 10$ mA, $I_B = 0.5$ mA $I_C = 100$ mA, $I_B = 5.0$ mA
Base-Emitter Saturation Voltage	V _{BE(SAT)}	1 1	700 900	1 1	mV	$I_C = 10$ mA, $I_B = 0.5$ mA $I_C = 100$ mA, $I_B = 5.0$ mA
Base-Emitter Voltage	V _{BE(ON)}	580 —	640 725	700 770	mV	$V_{CE} = 5.0V, I_{C} = 2.0mA$ $V_{CE} = 5.0V, I_{C} = 10mA$
Collector-Cutoff Current	I _{CBO}	1 1	1 1	15 5.0	nΑ μΑ	V _{CB} = 30V V _{CB} = 30V, T _A = 150°C
Gain Bandwidth Product	f⊤	100			MHz	$V_{CE} = 5.0V, I_{C} = 10mA,$ f = 100MHz
Collector-Base Capacitance	C _{CBO}	_	3.0	_	pF	V _{CB} = 10V, f = 1.0MHz

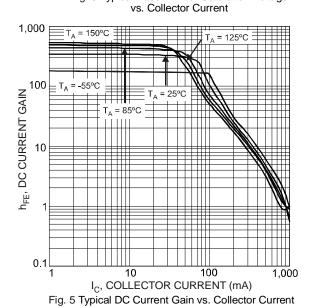
Notes: 1. No purposefully added lead.

- Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php
 Device mounted on FR-4 PCB, pad layout as shown on page 3, or Diodes Inc. suggested pad layout document AP02001 on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Short duration pulse test used to minimize self-heating effect.









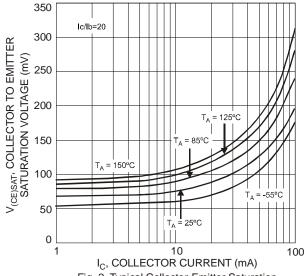
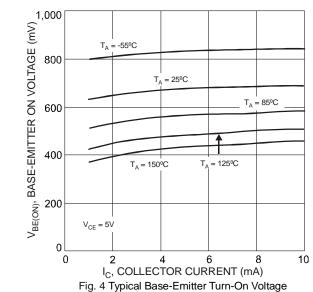


Fig. 2 Typical Collector-Emitter Saturation Voltage vs. Collector Current



vs. Collector Current



Ordering Information (Note 5)

Device	Packaging	Shipping
BC847BLP-7	DFN1006-3	3000/Tape & Reel

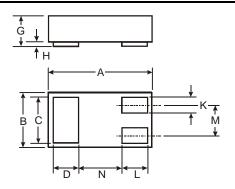
Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

1F

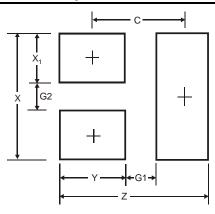
1F = Product Type Marking Code Dot Denotes Collector Side

Mechanical Details



DFN1006-3				
Dim	Min	Max	Тур	
Α	0.95	1.075	1.00	
В	0.55	0.675	0.60	
U	0.45	0.55	0.50	
D	0.20	0.30	0.25	
G	0.47	0.53	0.50	
Η	0	0.05	0.03	
K	0.10	0.20	0.15	
L	0.20	0.30	0.25	
М			0.35	
N			0.40	
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
X	0.7
X1	0.25
Y	0.4
С	0.7

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